1. To write on the the memory and cache simultaneously
2. Step1= …read…, step2=…erased…, step3=…written…
3. Vectored interrupts and multiple interrupting devices are not possible
4. Producing faster memory module
5. Offset = 4, set =10, tag =4
6. Lower internal clock speed of CPU
7. 256
8. Initation of interrupt
9. Memory to I/O units
10. SISD
11. 0x33
12. 0x400
13. 5MB/S

22Instruction set = complex, number of cloxks per instruction = multi-clock, code size of program = small code size, assembly code = simpler, bytes per instruction = different for variety of instructions

1. The multi-core processor and level off clock speed help to make heat dissipation of CPU

25.Mất đề

29. CPU and RAM

30. Larger instruction set and less registers

31.Write hit=write back, write miss=write allocate